

The diagram illustrates a 2D array structure. On the left, a grid is shown with rows labeled 'ROW 1', 'ROW 2', 'ROW 3', 'ROW 4', and 'ROW M'. The first four rows are each divided into four cells. To the right of the grid, a vertical column of four cells is shown, with an arrow pointing to the top cell labeled 'PIXEL N'. Above this column, four arrows point to the first four rows of the grid, labeled 'PIXEL 1', 'PIXEL 2', 'PIXEL 3', and 'PIXEL 4'. The entire structure is enclosed in a large oval labeled '20'.

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660660-11EE6E60

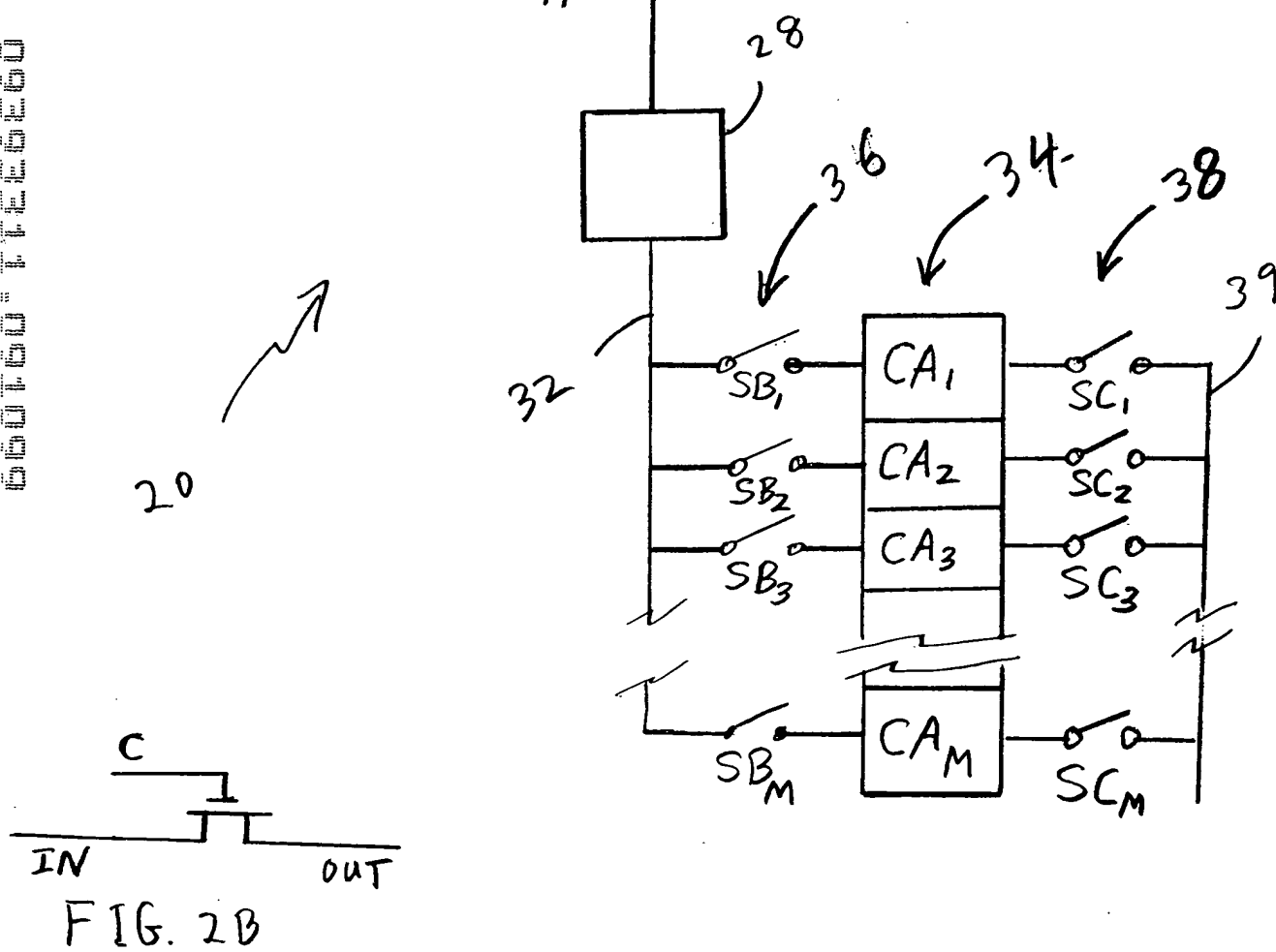
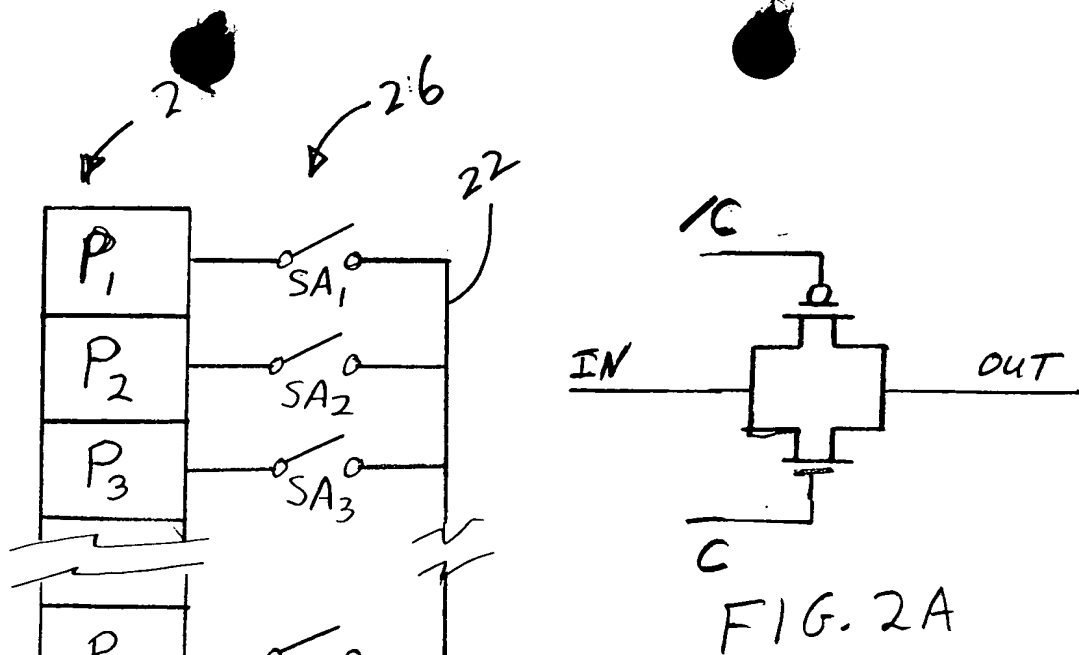
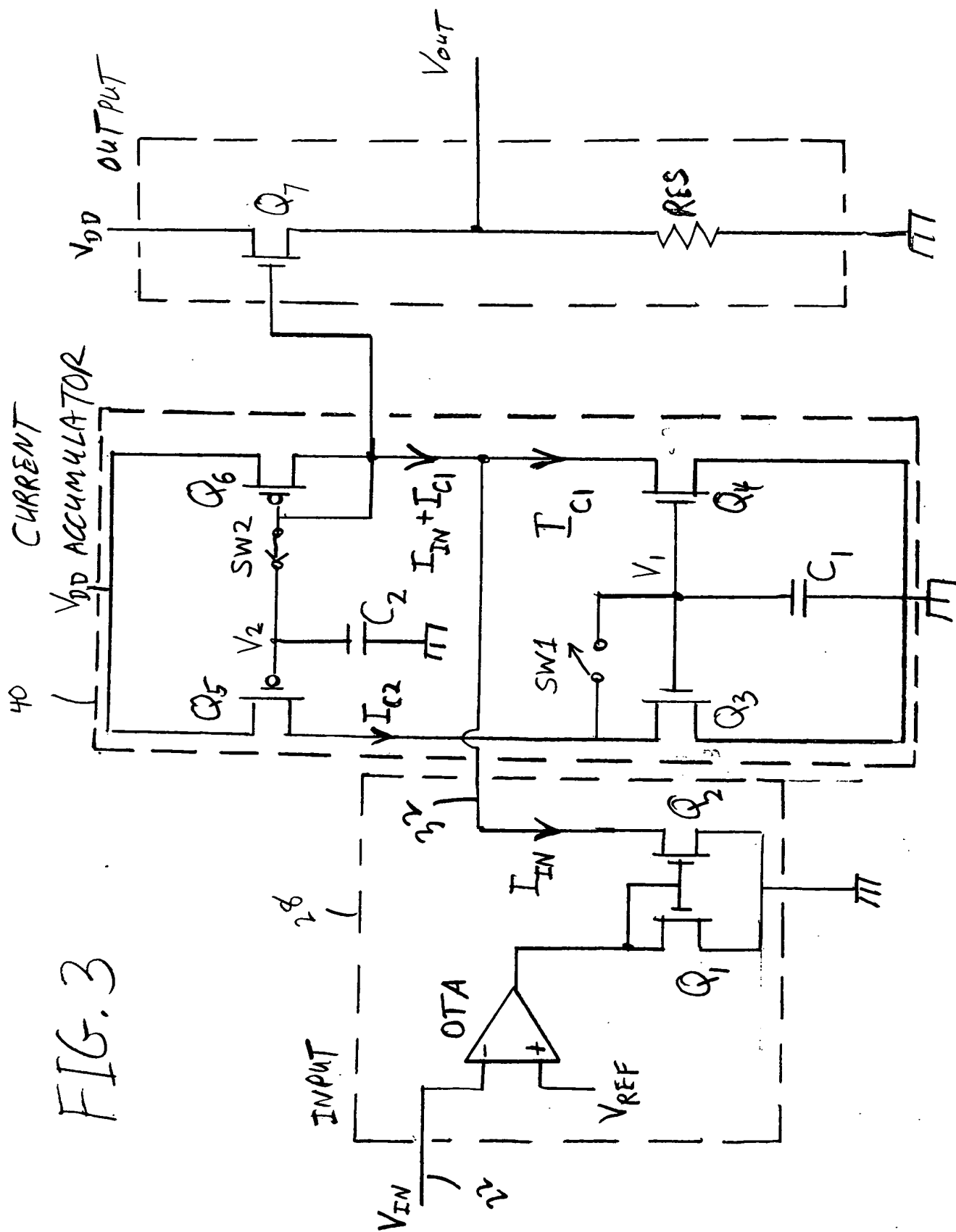


FIG. 2

FIG. 3



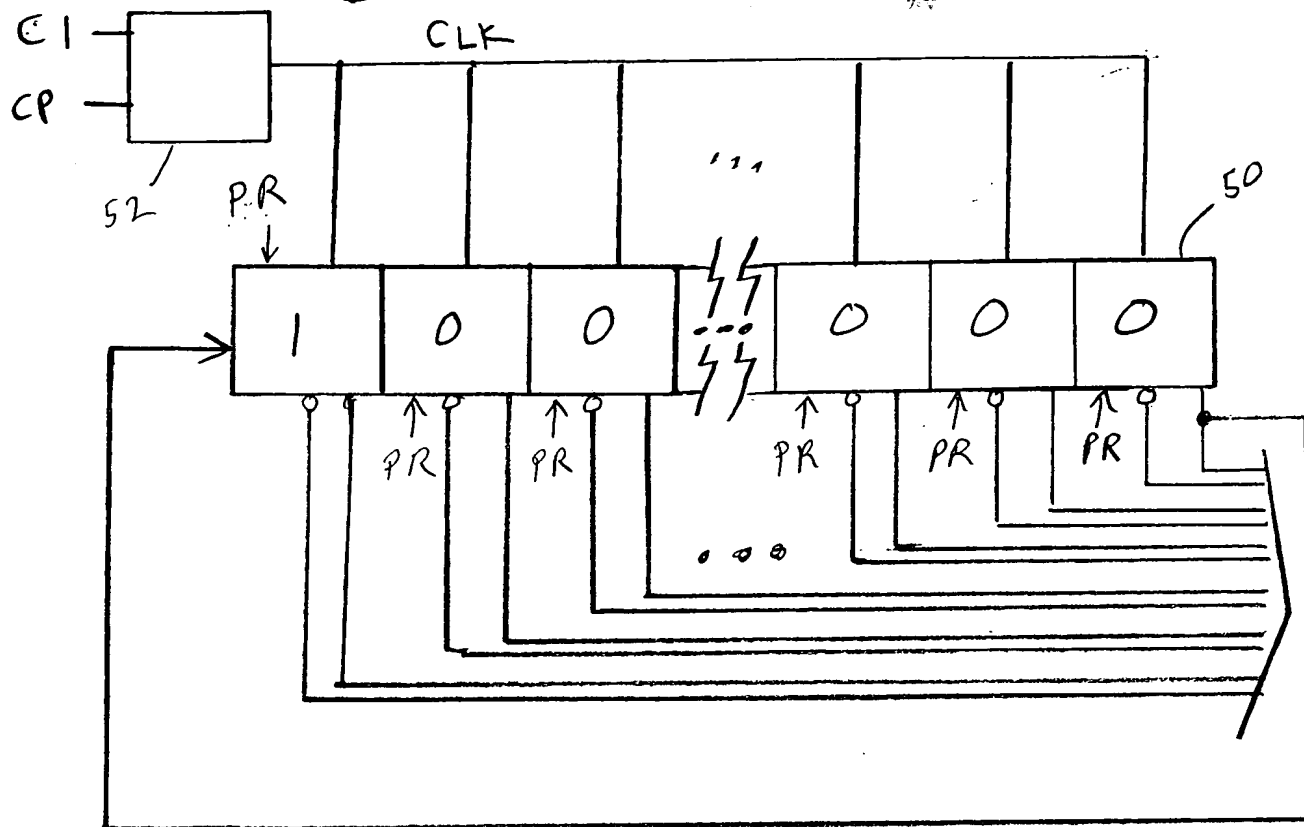
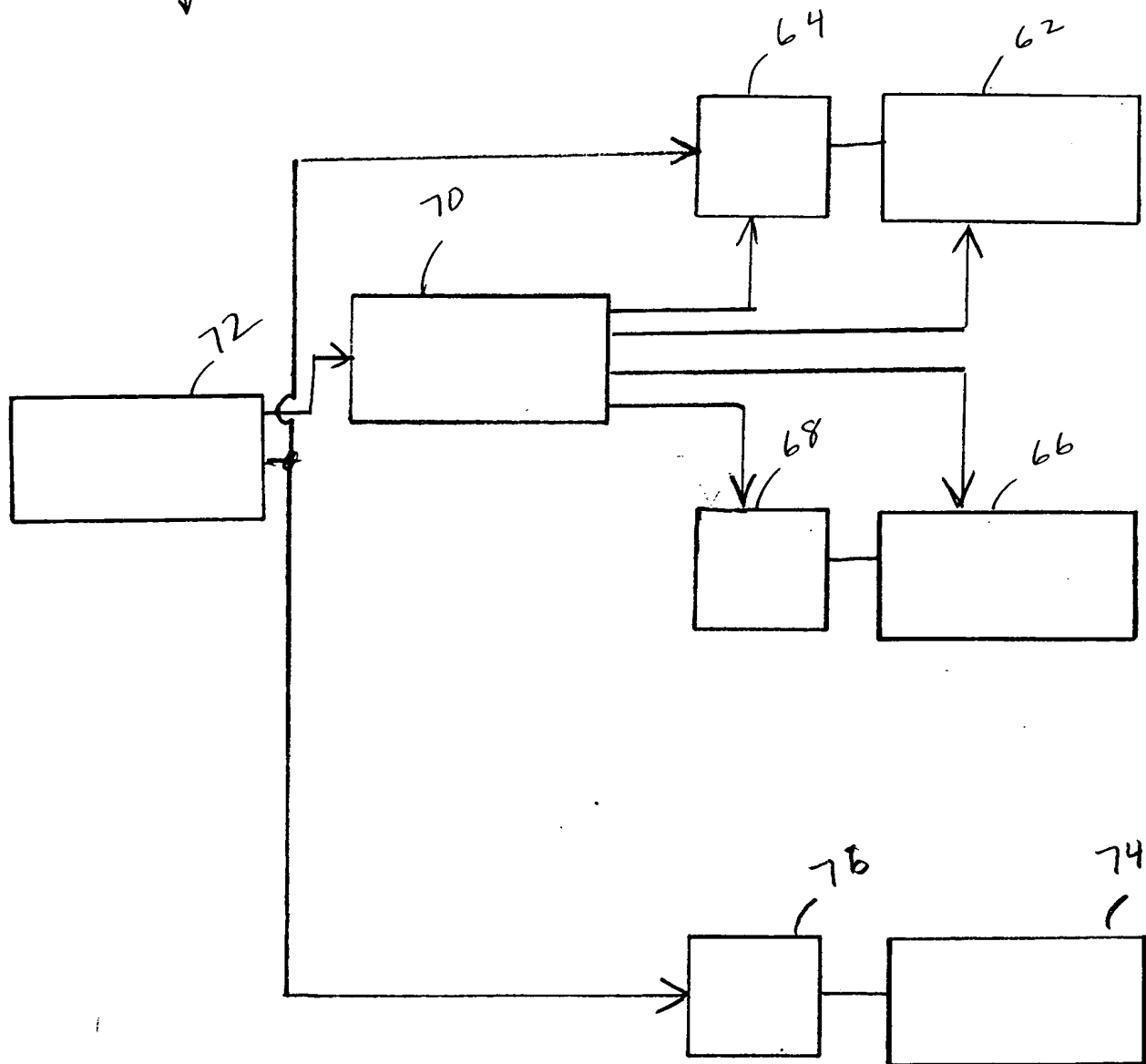


FIG 4

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FIG. 5



550T60" TEE6E60

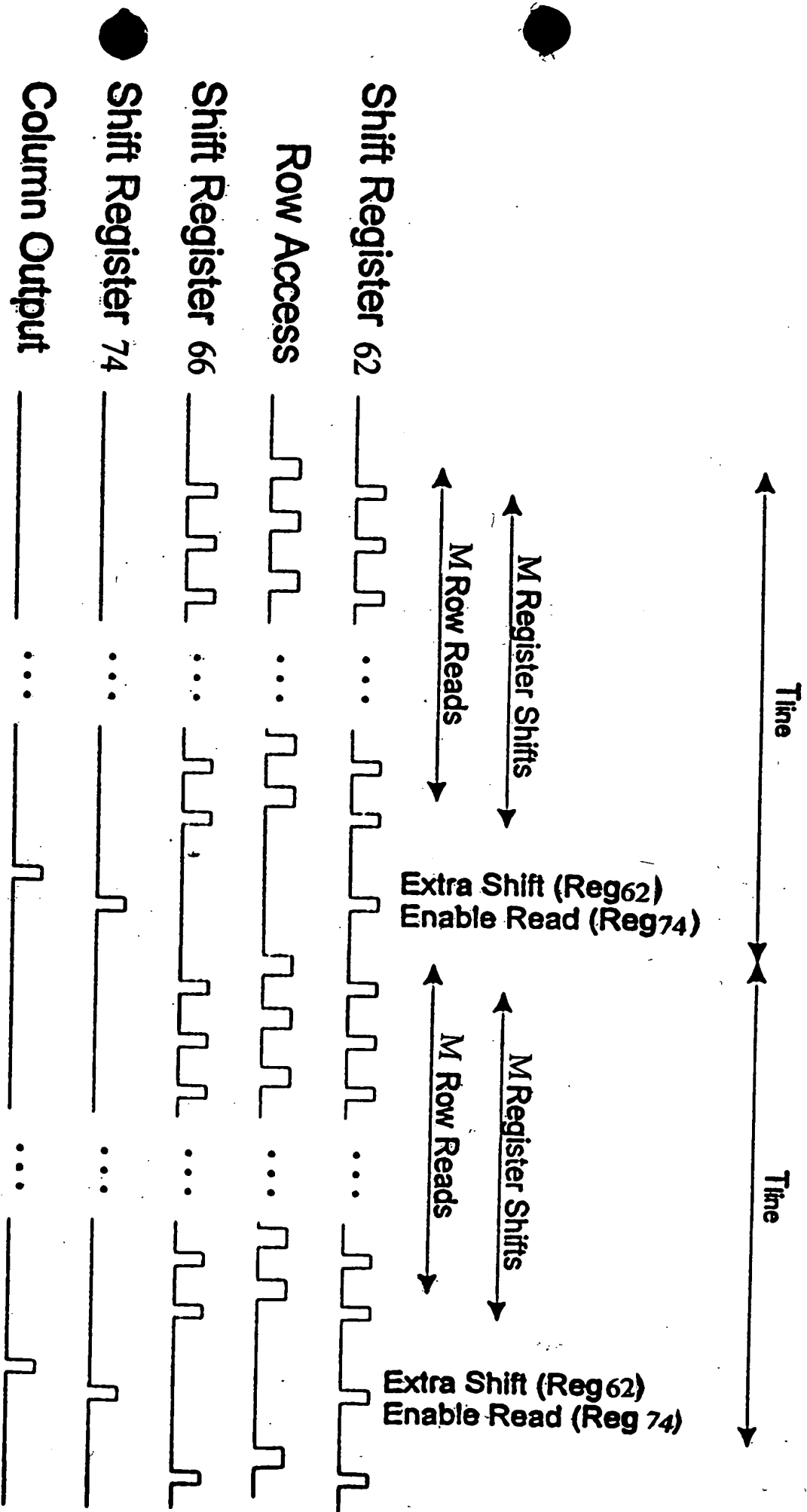


FIG. 6

START

FIG. 7

N1

N2

N3

S1

S11
COUPLE PIXEL ONTO
COLUMN BUS

S12
ROUTE COLUMN BUS
INTO ACCUMULATOR

S13
UPDATE
ACCUMULATOR

S14
S15
LAST
PIXEL IN
COLUMN?
YES
NO

INCREMENT FIRST
AND SECOND SWITCH
CONTROL CIRCUITS

S2

S21
END
OF FRAME?
YES
NO

S22
INCREMENT FIRST
AND THIRD SWITCH
CONTROL CIRCUIT

S23
OUTPUT VALUE

660160-1-EE660

